

Appln. No.: 10/055,120

Reply to Office action of December 23, 2005

**Amendments to the Specification:**

Please amend the two paragraphs under CROSS-REFERENCE TO RELATED APPLICATIONS on page 2 as follows:

The present application is a continuation of U.S. Patent application Serial No. 09/437,721, filed November 9, 1999, now U.S. Patent No. 6,363,129, which claims priority of the following provisional applications, the contents of each of which are herein incorporated by reference: Serial Number 60/107,874 entitled "Apparatus for, and Method of, Distributing Clock Signals in a Communications System" filed on November 9, 1998; Serial Number 60/108,319 entitled "Gigabit Ethernet Transceiver" filed on November 13, 1998; Serial Number 60/108,648 entitled "Clock Generation and Distribution in an Ethernet Transceiver" filed on November 16, 1998 and Serial Number 60/130,616 entitled "Multi-Pair Gigabit Ethernet Transceiver" filed on April 22, 1999.

The present invention is related to the following co-pending applications filed on the same day as the present invention and assigned to the same assignee, the contents of each of which are herein incorporated by reference: Serial Number 09/437,724 entitled "Switching Noise Reduction in a Multi-Clock Domain Transceiver", now U.S. Patent No. 6,307,905, and Serial Number 09/437,719 entitled "Multi-Pair Gigabit Ethernet Transceiver", now U.S. Patent No. 6,477,200.